### November 2021 update on Chicago Group Tasks

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## 1 Supporting the Fermilab upgrade to the Test Beam Facility

Fermilab has embarked on upgrading the Time-of-Flight (TOF) system at the Fermilab Test Beam Facility (FTBF) with the goal of improving the time resolution to better than 10 psec, with coverage over the whole beam profile. The initial installation was to be Evan Angelico's Ph.D thesis [1]; Evan had just installed inside the enclosure and taken first data when the Lab closed due to the pandemic. The effort has now been revived with the hire of Joe Pastika to lead the project. The PSEC group at Chicago, with Evan's help, is in the process of transferring the technology to the Lab.

### 1.1 Upgraded digitizing electronics

Two students, Toby Abelmann and Marek Michulka., have been working closely with Pastika and Paul Rubinov from Fermilab, Evan (now at Stanford), and Frisch on commissioning the Rev C ACDC front end waveform sampling cards. Rev C, which like its predecessor has 30 channels of PSEC4 chips sampling at 10-15 GS/sec at 10.5 bits, differs in that it provides six independent trigger thresholds per chip rather than only one, and has redesigned power supplies for significantly lower power consumption.

Sixteen Rev C cards have been stuffed. Abelmann and Michulka, supervised by Pastika and Rubinov, have been debugging the new power sections, and are now ready to implement code for the multiple trigger thresholds. In addition to use at the FTBF, these cards may be useful to the ANNIE experiment where self-triggering plays an important role.

Figure 1 shows the Rev C cards and the test setup in the Institute Electronics Development Group shop.



Figure 1: The REV C cards and test setup in the EFI EDG (Abelmann, Michulka).

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### 1.2 3Ghz sub-mm resolution 8-inch pad-based anodes

The LAPPD detector development used a strip-line readout of 50-ohm RF striplines for excellent time and spatial resolution [2, 3]. The pad development is recent and is intended for calorimeters and similar high-occupancy applications. The original (Incom 'Gen-I') LAPPD design was with an internal anode; the Test Beam installation uses capacitively-coupled 'Gen-II' LAPPDs, in which an external signal pickup anode board is capacitively coupled to a monolithic internal metal anode plane[4]. This allows the MCP-PMTs to be produced independent of the specific needs of each application for anode segmentation and RF characteristics (e.g. pads vs strips).

The 8-by-8 arrays of 1-in pads (64 pads total) for a rectangular pad (Left) and a submm spatial resolution patterned pad (Right) are shown in Figure 2. The patterned anode enhances the sharing of charge, enabling a sub-mm spatial resolution over the full area [5].

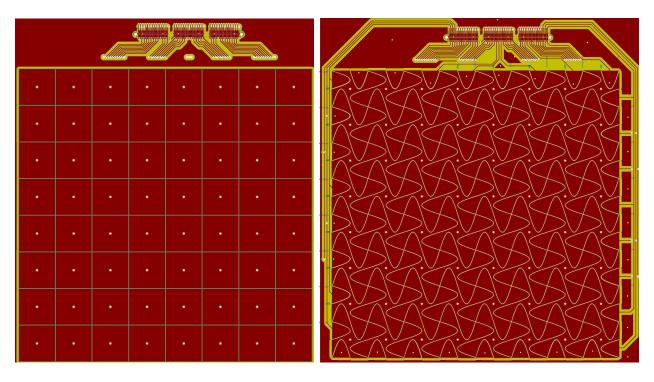


Figure 2: The layout of the top layer of an 8 by 8 array of signal pickup pads for a capacitively-coupled LAPPD $^{TM}$  [4]. The left-hand plot shows 1-inch square pads; the right-hand plot shows one implementation of a patterned anode that enhances the sharing of charge for improved spatial resolution [5] (J. Li, J. Park, E. Spieglan)

Figure 3 shows a map of the resolution of the sinusoidally patterned anode, given by the color bar on the right. With pads on a 1-inch pitch we expect sub-mm resolution for the noise levels of our current PSEC4 electronics [5].

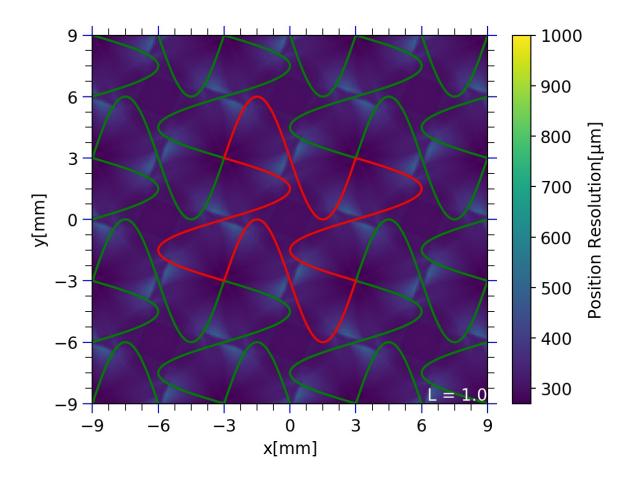


Figure 3: A map of the predicted position resolution of the 8-by-8 anode signal pickup board with patterned 1-inch pads for enhanced signal sharing [5]. A single pad is outlined in red. (J. Park)

#### 1.3 3Ghz Sub-mm resolution 200mm strip-based anodes

Impedance-matched RF strip-lines are a highly efficient anode pattern in terms of channel count, and also may be implemented to exploit an elegant method of optimizing time and space resolution.

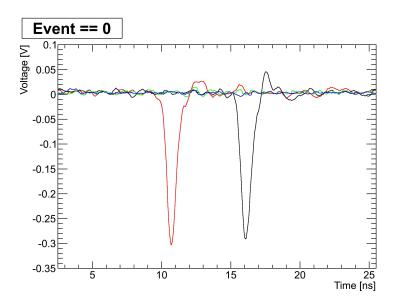


Figure 4: A PSEC4 waveform trace of an LAPPD pulse measured at one end of a 50-ohm RF stripline. The far end of the stripline was extended by cable and left unterminated; the 2nd pulse is the reflection. The distance between the first and second appearances of the pulse maps onto the position of the pulse on the stripline; the average time of the two is the time [2]. Having both pulses travel on the same strip and through the same network, and digitized by the same PSEC4 channel eliminates the need for channel-to-channel calibrations (E. Oberla, A. Elagin.)

Figure 5 shows the layout for the 50-ohm RF stripline anode. The strips double back on themselves on an internal layer with the same impedance as the top layer to provide the needed separation of the direct and reflected pulses when the shower is close to the far end of the strip [3].

### 1.4 Submission for fabrication; testing

The designs for the three patterns, square 1-inch pads, sine-wave pads on a 1-inch pitch, and 50-ohm striplines [2] are ready for submission to PC houses. The square and sine-wave pick-up boards are six layers. We will go out for quotes shortly after an internal 'Godparent' review with (among others) Pastika, Rubinov, Angelico, and F. Tang (EFI-EDG).

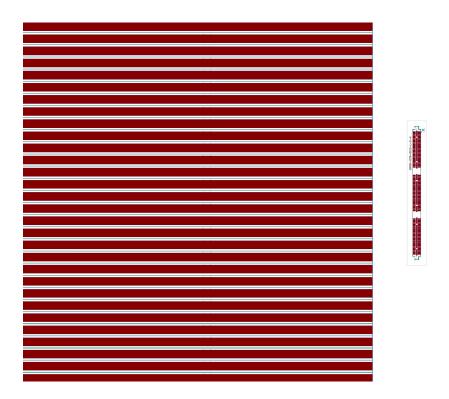


Figure 5: The 50-ohm RF stripline anode. The strips double back on themselves on an internal layer with the same impedance as the top layer to provide the needed separation of the direct and reflected pulses when the shower is close to the far end of the strip. (J. Li)

### 1.5 3Gz bandwidth, few picosecond resolution, waveform sampling

The PSEC4 15 GS/sec ASIC was made in the 130nm IBM 8RF process that is now completely obsolete. Waveform sampling is the gold-standard of fast timing [6]. We have opened discussions with Fermilab on developing a multi-channel 3GHz band-width ASIC. A draft of the specs is given in Table 1.5

Front-end							
Parameter	Min Value	Max Value	Units	Comment			
Analog Bandwidth	3	_	GHz	For 1ps res. per S. Ritt [7]			
Input	_	-	_	Single-ended (bypass gd to MCP2-out)			
Input impedance	50	200	Ohms	50 for current strips			
Voltage Range	300	1000	mv	Max signal: $S/N >> 100$			
Noise	_	1	mv	For 1ps res. per S. Ritt [7]			
Charge Range	3.2	160	pC	Max integrated charge			
Charge Resolution	10	10	bits	set by 10% of 1% sharing			
Cross-talk	_	0.1	%	?? channel-channel			
Linearity	_	0.1	%	after calibration			
2-pulse separation	1	_	nsec	(CFD) 1-foot unterminated strip far end			
Trigger threshld	1	FS	mv	one per channel			
Trigger latency	_	10	nsec	WAG- negotiable if needed			
Temperature Coeff.	_	$10^{-6}$	$\%/\mathrm{degC}$	1ps FS for 10 degC			
Max Time Recorded	_	12	$\mu \mathrm{s}$	5 muon lifetimes??			
Readout Buffers	4	_	_	parallel buffers			
Channels/ASIC	8	_	_	negotiable			
Power	==	???	mW	per chip, full rate			
System and DAQ							

System and DAQ						
Digitization Time	_	20	$\mu s$ ???	multiple buffers, 200 KHz		
Readout Rate	200	_	$\mathrm{kHz}$	Set by MTest		
Strips or Pads	_	_	_	Either: Capacitive Coupling Pickup		
MCP Current return	_	_	_	Bypass caps to MCP-2 out		
Channels/FE card	32	64	channels	30 strips or 64 pads??		
Local Sparsification	_	_	_	Yes		
System # of channels	30	384	channels	1 strip MCP to 6 pad MCPs		
Clock	_	_	_	External; White Rabbit compatible		
Clock Jitter	_	$10^{-12}$	_	1 ps/s; e.g. WR PPS		

Table 1: A draft of specs for the 1-ps upgrade to the FTFB TOF system. FE refers to a possible Front-End card; BE refers to a Back-End card. The implemented architecture may differ. Note that the 3 GHz analog bandwidth spec refers to the complete FE signal path and not just the ASIC.

# 2 Batch Production of Capacitively Coupled LAPPD $^{TM}$ MCP-PMTs

We have developed an air-transfer process for MCP-PMT production that closely follows the steps used in batch production of standard bialkali-photocathode photomultipliers [8, 9, 10]. We later learned that our process is both well-supported theoretically and is being used by the Russian PMT firm MELZ to make commercial photomultipliers.

At the start of the LAPPD project we hired a young SULI student at ANL, Camden Ertley, who after working with us for a year went on to get his Ph.D in space sciences at UNH. After working with O. Siegmund at SSL at Berkeley and Incom, Dr. Ertley is now at Southwest Research Institutes, and, like us, is convinced that the original concept of a low-cost high-yield batch-produced 'frugal tile' is achievable. Figure 6 shows the two 'Margherita' single-tile fabrication prototypes for batch production that would provide the basis for the SWRI design. Current collaborative design efforts are on exploiting the air-transfer technique to produce photocathodes with QE's substantially higher than industry best for alkali cathodes, on simplifying the stack-up and hermetic housing, and on enhanced amplification stacks.



Figure 6: The two Margherita single-tile fabrication prototypes for batch production [9, 10]. With C. Ertley of Southwest Research Institutes we are developing a design of 6 stacks of 3 LAPPDs each per cart, with a 1 week cycle. Six carts would produce 100 tiles per week.

### 3 Energy-separated TOF-tagged Neutrino Beams

An idea for simultaneously running neutrino oscillation experiments with multiple neutrino energy spectra grew out of a joint Fermilab-Chicago workshop on the uses of fast timing held at the University [11]. The precise localization of ionization by the LAr TPCs allows a one-parameter fit to the time at the neutrino detector even with very small coverage [12]. With Matt Wetstein (ISU) and Evan Angelico (Stanford) we continue to pursue this idea with several young scientists interested in Early Career Awards at Fermilab. While not a major time sink, a number of us believe it's an important set of ideas and needs to be kept active during the construction of the Fermilab neutrino detectors for reduction of systematics in a competitive environment.

# 4 Neutrinoless Double-Beta Decay, charged particle tracking, low-energy electron energy and direction measurement, PET

In addition to our DOE-supported effort on the TOF upgrade at the FTFB, with University financial support Shida, Spieglan, Domurat-Sousa, and Frisch are collaborating with A. Squires (Molecular Engineering), V. Rawal (Chemistry), and Patrick LaRiviere (Radiology) to develop a non-cryogenic liquid medium to measure charged particle tracks at 10-micron spatial resolution and 1-2% energy resolution for 1 MeV electrons [13, 14]. The motivation is to develop a cheap large high-precision detector for neutrinoless double-beta decay [15]. Current work is focusing on Positron-emission Tomography (PET), which has a similar signature of multiple low-energy electrons, but a faster time scale, better pay-off, and larger market. An attractive spin-off is the introduction to the TOPAS [16] wrapper for the Geant4 package, suggested to us by LaRiviere and being implemented for Compton scatters in PET by Domurat-Sousa. It is intended for use by the medical imaging community, but we see that it is very useful for detector development, and hope that we can participate in introducing it to the HEP detector community.

### 5 Acknowledgements

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